

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Patent Application Serial No. .... 10/783,419  
Confirmation No. .... 6384  
Filing Date ..... February 19, 2004  
Inventor ..... Yoshiki Hishiro  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2814  
Examiner ..... Ginette Peralta  
Attorney Docket No. .... MI22-2507  
Title: Methods of Forming Semiconductor Constructions

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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Dated: Sept. 29, 2004

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NUMBER OF PAGES IN FACSIMILE: 11

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Dated: 9/29/04

By: David G. Latwesen  
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PTO/SB/21 (09-04)

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Total Number of Pages in This Submission

11

Application Number

10783,419

Filing Date

February 19, 2004

First Named Inventor

Yoshiki Hishiro

Art Unit

2814

Examiner Name

Ginette Peralta

Attorney Docket Number

MI22-2507

**ENCLOSURES (Check all that apply)**☐ Fee Transmittal Form☐ Fee Attached☒ Amendment/Reply☐ After Final☐ Affidavits/declaration(s)☐ Extension of Time Request☐ Express Abandonment Request☐ Information Disclosure Statement☐ Certified Copy of Priority Document(s)☐ Reply to Missing Parts/Incomplete Application☐ Reply to Missing Parts under 37 CFR 1.52 or 1.53☐ Drawing(s)☐ Licensing-related Papers☐ Petition☐ Petition to Convert to a Provisional Application☐ Power of Attorney, Revocation☐ Change of Correspondence Address☐ Terminal Disclaimer☐ Request for Refund☐ CD, Number of CD(s) \_\_\_\_\_☐ Landscape Table on CD☐ After Allowance Communication to TC☐ Appeal Communication to Board of Appeals and Interferences☐ Appeal Communication to TC (Appeal Notice, Brief, Reply Brief)☐ Proprietary Information☐ Status Letter☒ Other Enclosure(s) (please identify below):

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Signature

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David G. Latwesen, Ph.D.

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Date

Sept. 29, 2004

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**RESPONSE TO JULY 14, 2004 OFFICE ACTION**

To: Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**By Facsimile 1 (703) 872-9306**

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